



PTO/SB/089 (07-05)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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**Complete if Known**

Application Number	10/722,255
Filing Date	11-24-2003
First Named Inventor	J. Davin MACKENZIE et al.
Art Unit	<del>2825</del> 2823
Examiner Name	Nicholas Toboys
Attorney Docket Number	KOV-015

Sheet 2 of 2

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
NT	CA	Kelaro YANO, Yusaka KITSUNO, Akira SAKAWAKI, and Keiji KAWASAKI; Formation of Silicon Membranes; Patent Abstracts of Japan; Publication Date: October 17, 1995; Publication No. 07267621 A; Japanese Patent Office, Japan	

Examiner Signature		Date Considered	12/1/06
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Examiner Name	N. Tobers
Attorney Docket Number	KOV-015

U. S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)				
NT  ↓	AA	US-	2003/0124259 A1	07-03-2003	KODAS et al.	
	AB	US-	6,348,295 B1	02-19-2002	GRIFFITH et al.	
	AC	US-	6,331,348 B1	12-18-2001	SAKAI et al.	
	AD	US-	6,277,169 B1	08-21-2001	HAMPDEN-SMITH et al.	
	AE	US-	5,989,945	11-23-1999	YUDASAKA et al.	
	AF	US-	5,164,565	11-17-1992	ADDIEGO et al.	
	AG	US-	5,132,248	07-21-1992	DRUMMOND et al.	
	AH	US-	4,933,204	06-12-1990	WARREN, Jr. et al.	
	AI	US-	4,526,807	07-02-1985	AUERBACH	
	AJ	US-	4,340,617	07-20-1982	DEUTSCH et al.	
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)					

Examiner Signature	Date Considered
<i>[Signature]</i>	12/1/06

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JS		TOTTORI ISAO; Semiconductor Device and Manufacture Thereof; Patent Abstracts of Japan; Application No. 08180501; Application Date 07-10-1996; Application Publication Date 01-27-1998; Japan Patent Office	

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